OFE 1403

IR-3347 (2-5556)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Confirmation No.: 8003

Gordon Munns

Date: December 4, 2008

U.S. Patent No.: 7,112,830

Issued: September 26, 2006

For: SUPER LATTICE MODIFICATION OF OVERLYING TRANSISTOR

Mail Stop PETITIONS Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

REQUEST TO CORRECT THE ASSIGNEE UNDER 37 C.F.R. §3.81(b)

Sir:

The Assignee of the above-identified issued patent hereby petitions the Commissioner under 37 C.F.R. §3.81(b) to permit correction of the name of the assignee after issuance of the patent.

A: Enclosed is our check No. <u>28695</u> in the amount of \$130.00 in payment of the processing fee set forth in 37 C.F.R. § 1.117(h).

B: The undersigned states that the failure to include the correct Assignee name of International Rectifier Corporation on the PTOL-85B was inadvertent.

C: A copy of the Notice of Recordation of Assignment Document is enclosed in connection with an Assignment that was submitted for recordation with the U.S. Patent and Trademark Office on August 1, 2006. The Assignment is from APA Enterprises Inc. to International Rectifier Corporation. The Assignment was recorded on August 1, 2006 at Reel/Frame 018039/0080.

If the Request is granted, it is respectfully requested that the file be forwarded to the Certificate of Correction Branch for issuance of a Certificate of Correction.

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In view of the foregoing, favorable consideration of this Request is respectfully requested.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Mail Stop Petitions, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on December 4, 2008:

Samuel H. Weiner

Name of applicant, assignee or

Registered Representative

Signature

December 4, 2008

Date of Signature

SHW:JJF:ck

Respectfully submitted,

Samuel H. Weiner

Registration No.: 18,510

OSTROLENK, FABER, GERB & SOFFEN, LLP

1180 Avenue of the Americas

New York, New York 10036-8403

Telephone: (212) 382-0700





UNITED STATES PATENT AND TRADEMARK OFFICE

UNDER SECRETARY OF COMMERCE FOR INTELLECTUAL PROPERTY AND DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE

AUGUST 02, 2006

PTAS

500133051A

OSTROLENK, FABER, GERB & SOFFEN, LLP 1180 AVENUE OF THE AMERICAS NEW YORK, NY 10036

500133051A

UNITED STATES PATENT AND TRADEMARK OFFICE NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 571-272-3350. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, MAIL STOP: ASSIGNMENT SERVICES BRANCH, P.O. BOX 1450, ALEXANDRIA, VA 22313.

RECORDATION DATE: 08/01/2006

REEL/FRAME: 018039/0080

NUMBER OF PAGES: 4

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

DOCKET NUMBER: IRO.001 (2-1) V1062

ASSIGNOR:

APA ENTERPRISES, INC.

DOC DATE: 08/01/2006

ASSIGNEE:

INTERNATIONAL RECTIFIER CORPORATION

233 KANSAS STREET

EL SEGUNDO, CALIFORNIA 90245

SERIAL NUMBER: 10723382

FILING DATE: 11/25/2003

ISSUE DATE:

PATENT NUMBER: TITLE: SUPER LATTICE MODIFICATION OF OVERLYING TRANSISTOR Rightrax 8/2/06 10:12 PAGE 003/005 Fax Server

018039/0080 PAGE 2

SERIAL NUMBER: 11450806 FILING DATE: 06/08/2006

PATENT NUMBER: ISSUE DATE:

TITLE: SUPER LATTICE MODIFICATION OF OVERLYING TRANSISTOR

SERIAL NUMBER: 07701792 FILING DATE: 05/17/1991 PATENT NUMBER: 5192987 ISSUE DATE: 03/09/1993

TITLE: HIGH ELECTRON MOBILITY TRANSISTOR WITH GAN/ALXGA1-XN HETEROJUNCTIONS

SERIAL NUMBER: 08026528 FILING DATE: 03/03/1993
PATENT NUMBER: 5296395 ISSUE DATE: 03/22/1994
TITLE: METHOD OF MAKING A HIGH ELECTRON MOBILITY TRANSISTOR

ASSIGNMENT SERVICES BRANCH $\gamma + \gamma$ PUBLIC RECORDS DIVISION

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PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1 08/01/2006 500133051

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SUBMISSION TYPE:	NEW ASSIGNMENT

ASSIGNMENT

CONVEYING PARTY DATA

NATURE OF CONVEYANCE:

Name	Execution Date
APA Enterprises, Inc.	08/01/2006

RECEIVING PARTY DATA

Name:	International Rectifier Corporation	
Street Address:	233 Kansas Street	
City:	El Segundo	
State/Country:	CALIFORNIA	
Postal Code:	90245	

PROPERTY NUMBERS Total: 4

Property Type	Number	
Patent Number:	5192987	
Application Number:	10723382	
Application Number:	11450806	
Patent Number:	5296395	

CORRESPONDENCE DATA

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IR0.001 (2-1) V1062

NAME OF SUBMITTER:

Samuel H. Weiner

Total Attachments: 2

OP \$160.00 519298

ITRUCLAY

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source=00779602#page1.tif source=00779602#page2.tif WHEREAS, APA Enterprises, Inc., a corporation organized and existing under the laws of the state of Minnesota and having a principal place of business at 2950 N.E. 84th Lane, Blaine, Minnesota 55434 (hereinafter referred to as the "ASSIGNOR") is the owner of the U.S. patents and applications for United States Letters Patent set forth in SCHEDULE A annexed hereto; and

WHEREAS, INTERNATIONAL RECTIFIER CORPORATION, a corporation organized and existing under the laws of the state of Delaware and having a principal place of business at 233 Kansas Street, El Segundo, California 90245 (hereinafter referred to as the "ASSIGNEE") is desirous of acquiring all right, title and interest in said patents and applications for patent, the inventions set forth therein, and any patent that may be granted therefor.

NOW, THEREFORE, in consideration of One Dollar (\$1.00) and other good and valuable consideration, the receipt of which is hereby acknowledged, ASSIGNOR does hereby sell, assign and set over to said ASSIGNEE the entire right, title and interest for the United States and all other countries in and to said inventions, and the aforesaid patents and applications for patent, and all original, divisional, continuation, substitute, reissue and reexamination applications for patents applied for or granted therefor in the United States and all other countries, including all rights of priority from the filing of said inventions, and all rights for past infringement, and the Commissioner of Patents and Trademarks is hereby authorized and requested to issue all patents on said inventions or resulting therefrom to said ASSIGNEE herein as ASSIGNEE of the entire interest therein; and the undersigned for itself and its legal representatives, heirs, and assigns does hereby agree and covenant without further remuneration, to execute and deliver all divisional, continuation, substitute and reissue United States and other country applications for patent on said inventions and all assignments thereof to said ASSIGNEE or its assigns, to communicate to said ASSIGNEE or its representatives all facts known to the undersigned respecting said inventions, whenever requested to testify in any Interferences or other legal proceedings in which any of said applications or patents may become involved, to sign all lawful papers, make all rightful oaths or declarations, and to do generally everything necessary to aid said ASSIGNEE, its successors, assigns, and nominees to obtain patent protection for said inventions in the United States and all other countries, the expenses incident to said applications to be borne by and paid by said ASSIGNEE.

APA Enterprises, Inc. 2950 N.E. 84th Lane Blaine, Minnesota 55434

Anil K. Jain President

Hugust 1.

Date

SCHEDULE A

U.S. Patent No./U.S. Serial No.	Issue Date/Filing Date	Title
5,192,987	March 9, 1993	HIGH ELECTRON MOBILITY
		TRANSISTOR WITH GAN/
		Al _x Ga _{1-x} N
5,296,395	March 22, 1994	METHOD OF MAKING A HIGH
		ELECTRON MOBILITY
		TRANSISTOR
10/723,382	November 25, 2003	SUPER LATTICE
		MODIFICATION OF OVERLYING
		TRANSISTOR
11/450,806	June 8, 2006	SUPER LATTICE
		MODIFICATION OF OVERLYING
		TRANSISTOR